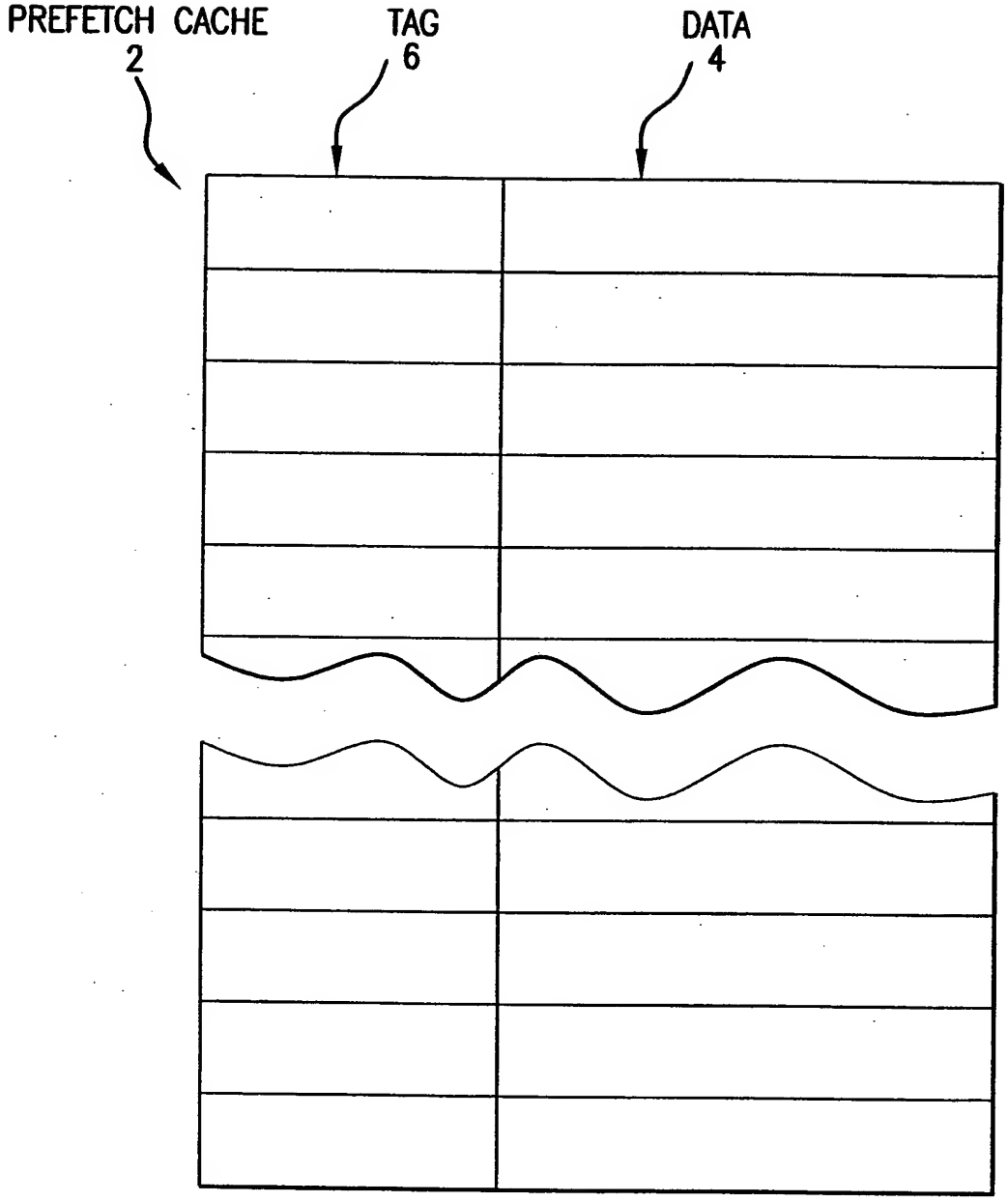


FIG. 1

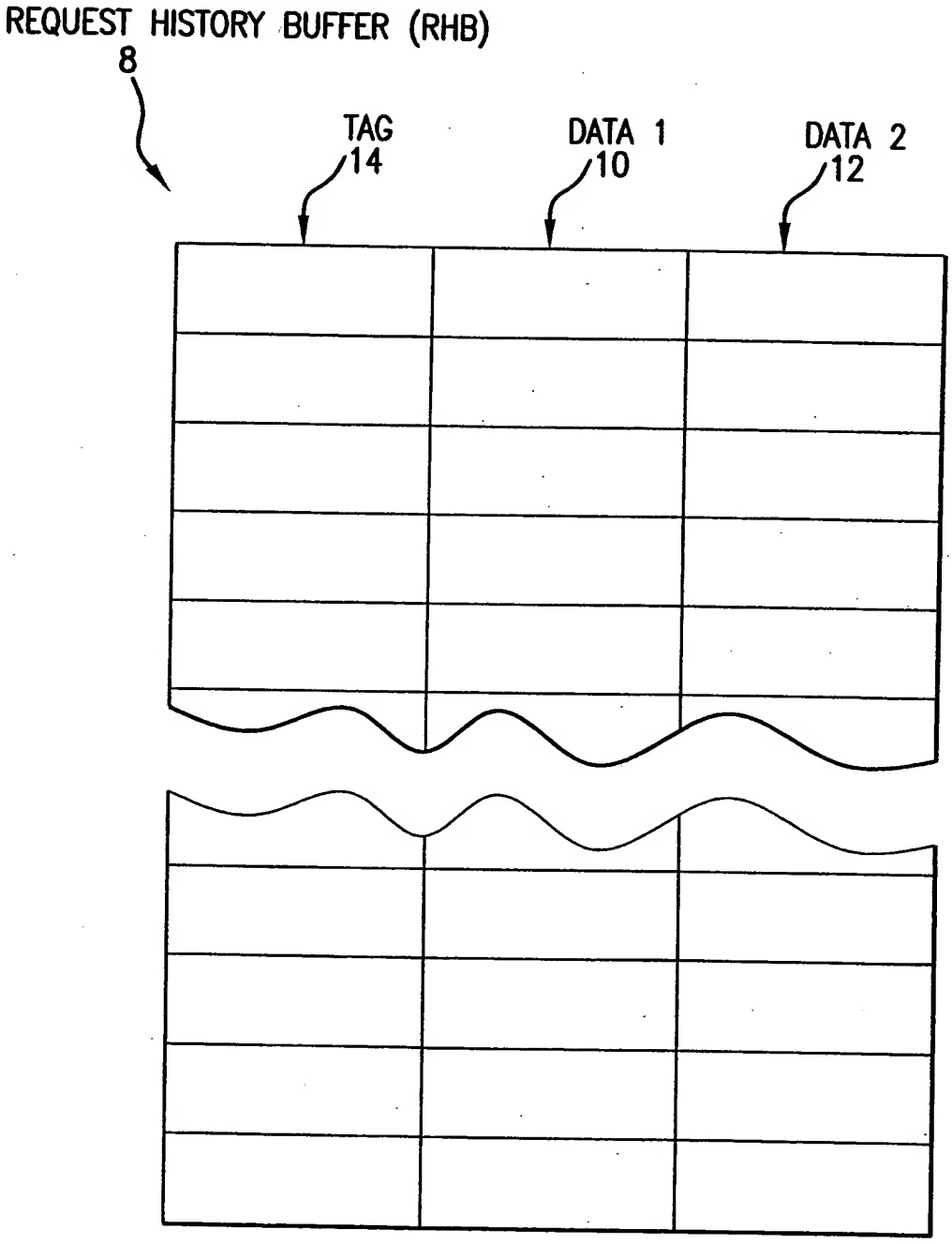
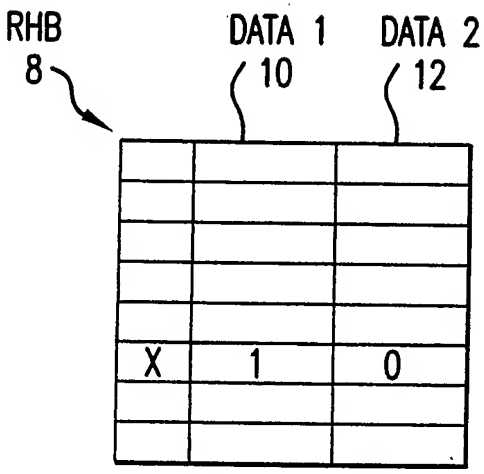
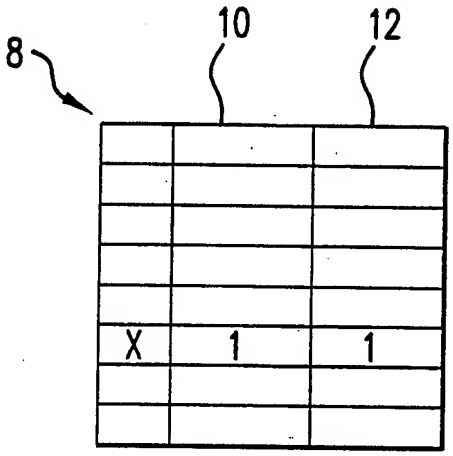
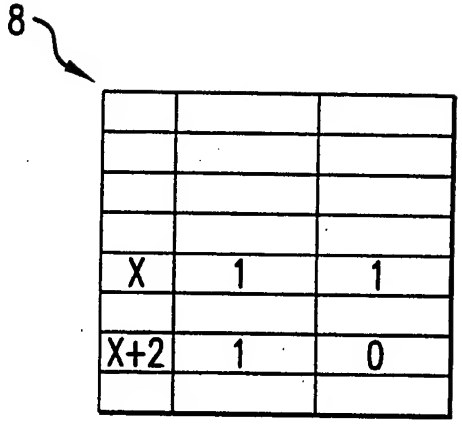


FIG.2







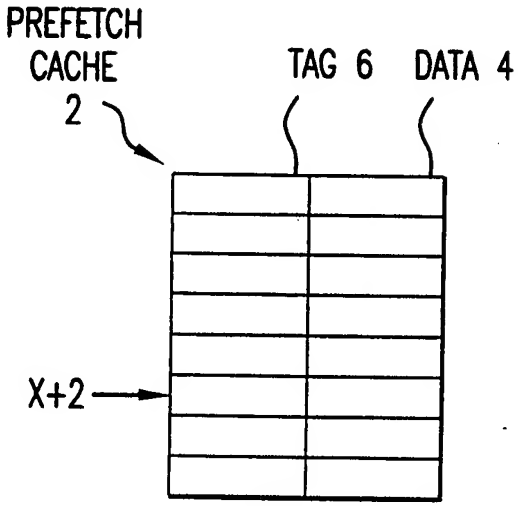


FIG.3A

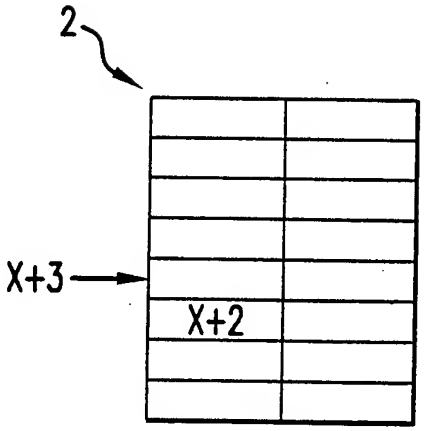


FIG.3B

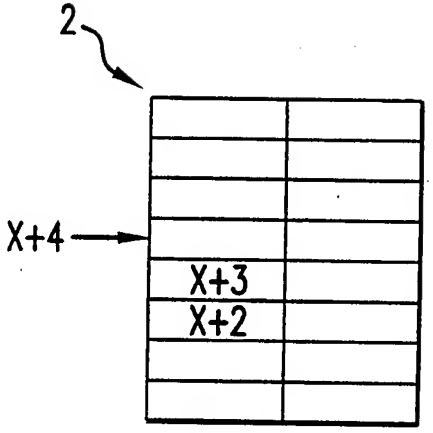
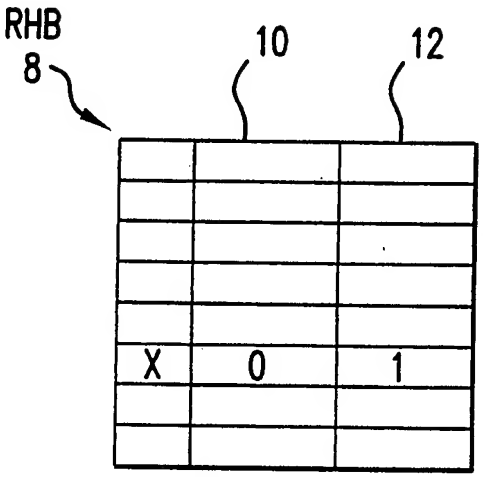
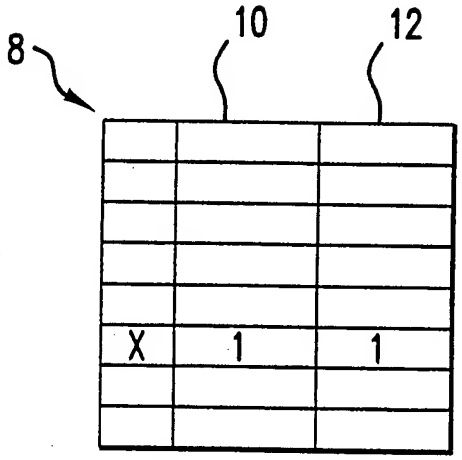
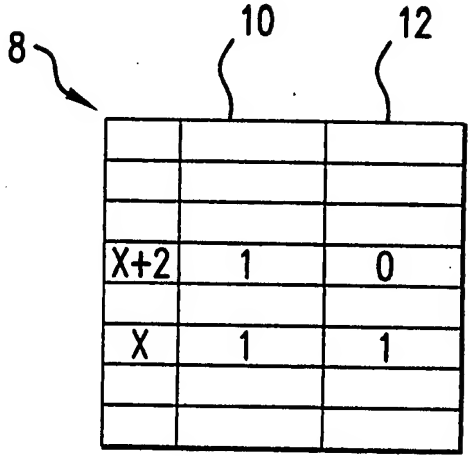


FIG.3C







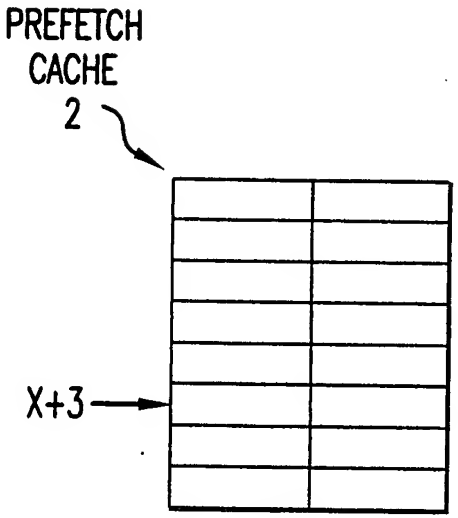


FIG.4A

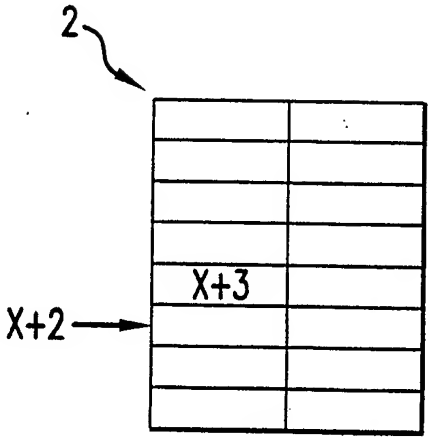


FIG.4B

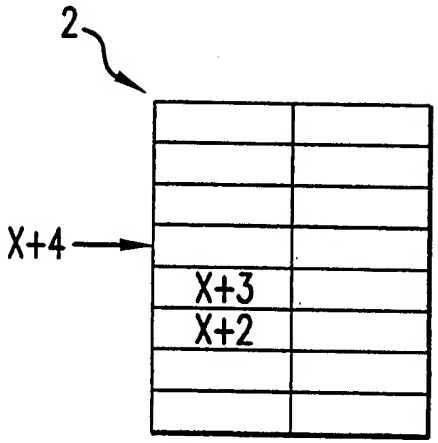


FIG.4C

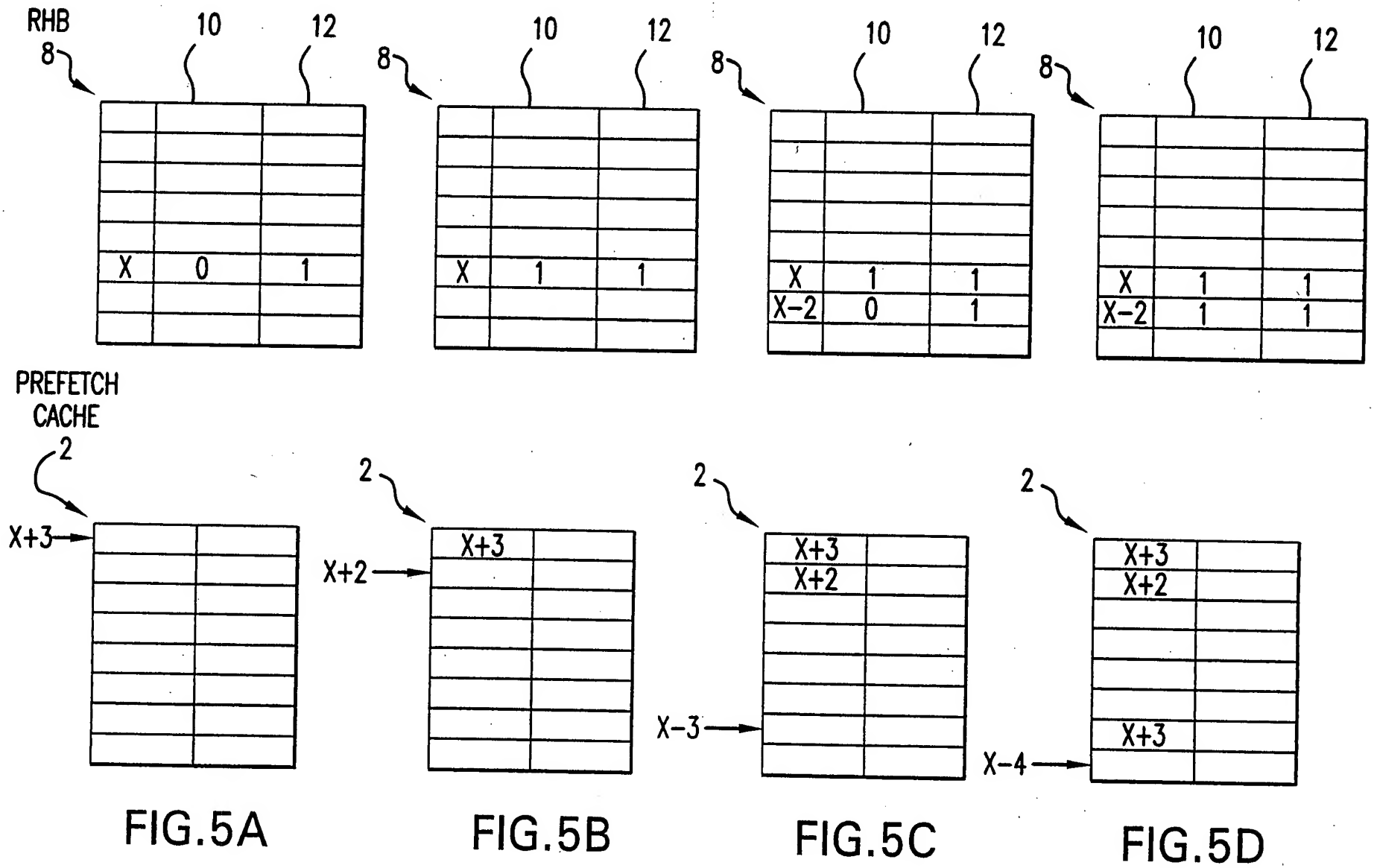
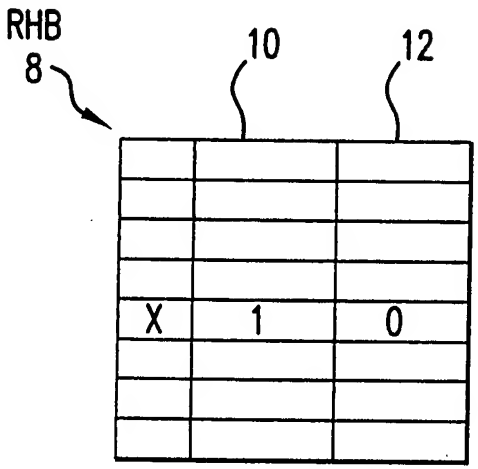
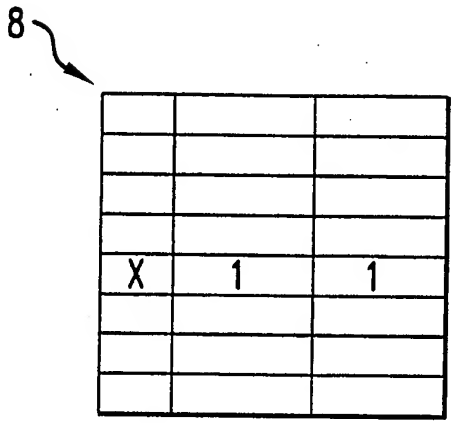
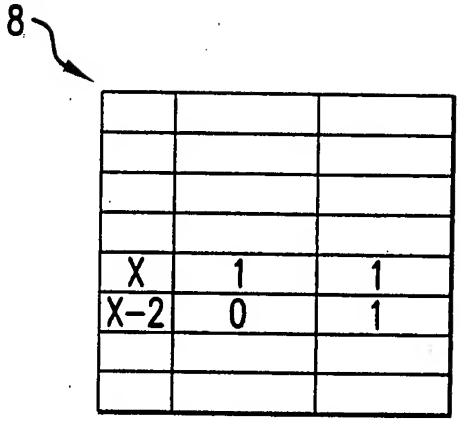


FIG.5A FIG.5B FIG.5C FIG.5D







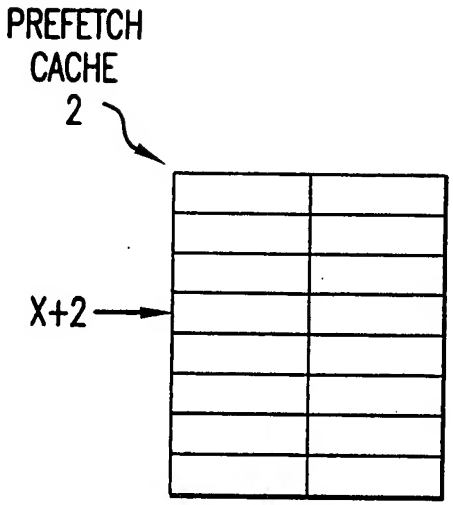


FIG.6A

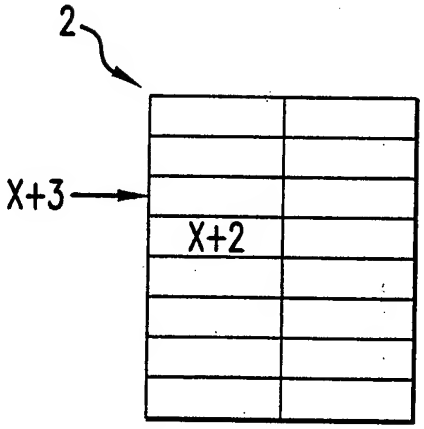


FIG.6B

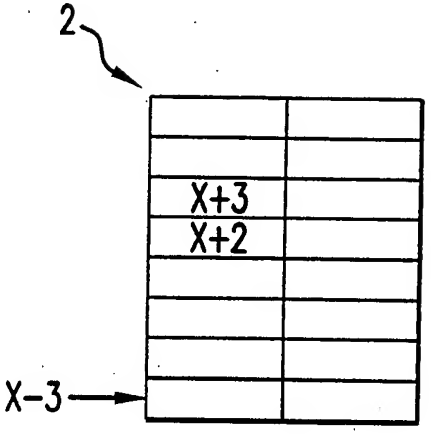


FIG.6C

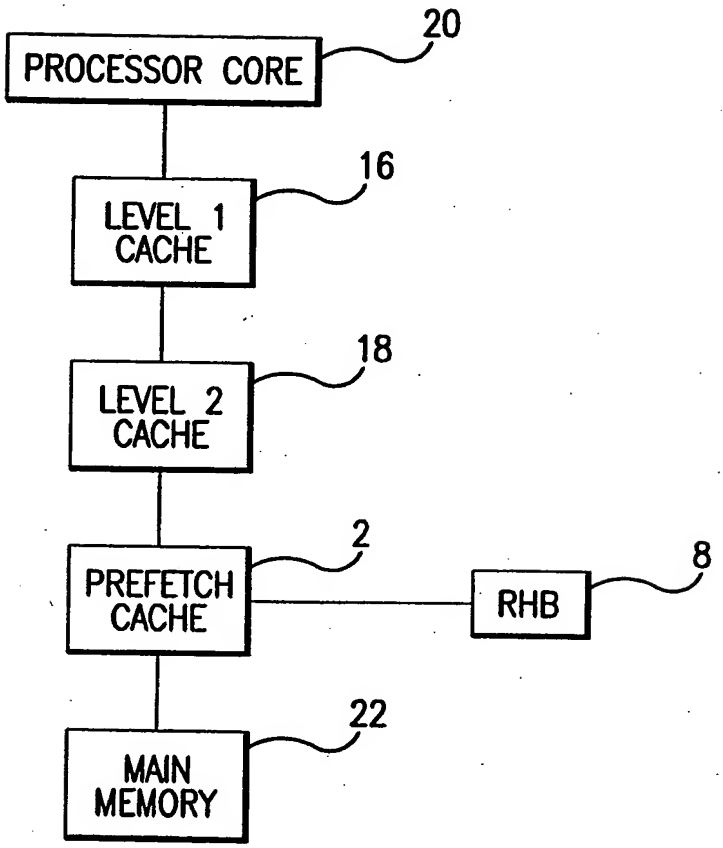

PROCESSOR CORE
20
LEVEL 1 CACHE
16
LEVEL 2 CACHE
18
PREFETCH CACHE
2
RHB
8
MAIN MEMORY
22

FIG.7